

# Semester\_2\_Draft\_TT2024\_2025

## Course timetable - PHYS 2402, Digital Circuits & Logic Design (Wks S2W1-S2W12)

	08:00AM	09:00AM	10:00AM	11:00AM	12:00PM	01:00PM	02:00PM	03:00PM	04:00PM	05:00PM	06:00PM	07:00PM	08:00PM	09:00PM
Mon														
Tue														
Wed		Lecture, Wks S2W1-S2W12 [=12] Course: PHYS 2402 Staff: SHARMA, Davinder Room: FST 412												
Thursday		Lecture, Wks S2W1-S2W12 [=12] Course: PHYS 2402 Staff: SHARMA, Davinder Room: FST 412												
Friday					Lecture, Wks S2W1-S2W12 [=12] Course: PHYS 2402 Staff: SHARMA, Davinder Room: FST 412									

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Sat														
Sun														